

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Mohamed Azimane et al.

Group Art Unit: 2117

Serial No.: 10/591,193

Examiner: McMahon, Daniel F.

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For: DFT TECHNIQUE FOR STRESSING SELF-TIMED
SEMICONDUCTOR MEMORIES TO DETECT DELAY
FAULTS

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO OFFICE ACTION

Sir:

In response to the Office Action mailed September 4, 2008, please find the following:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 9 of this paper.

Remarks/Arguments begin on page 15 of this paper.

CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Date: _____

Signed: _____

Typed Name: Mark A. Wilson